

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re The Application of: John B. DuffielII et al.	) )
Serial No.: 10/087,376	) Examiner: Daftuar, Saket K.
Filed: March 1, 2002  For: METHOD TO OPTIMIZE THE	) ) Art Unit: 2151
LOAD BALANCING OF PARAL- LEL CO-PROCESSORS	) ) )
	Cesari and McKenna, LLP 88 Black Falcon Avenue Boston, MA 02210 December 19, 2006
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